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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

RECEIVED

In Re Application of:)
KEVIN K. FUNK)
Serial No. 09/643,389 ✓)
Filed August 22, 2000 ✓)
For: APPARATUS AND METHOD FOR)
MANUFACTURING INTEGRATED)
CIRCUIT DEVICES)

JAN 21 2003
Technology Center 2100
Group Art Unit 2125
Examiner M. Von Buhr
Atty Dkt 10970997-3

RESPONSE TO FINAL OFFICE ACTION DATED NOVEMBER 19, 2002

To the Assistant Commissioner for Patents
Washington, D.C. 20231

This is in response to the Final Office action
mailed November 19, 2002, in the above-referenced
application.

REMARKS

Claims 13-18 were pending in the application prior
to this response. Claims 13-18 remain in the application
unchanged. Reexamination and reconsideration are
requested.

I. Rejection of Claim 13 under 35 U.S.C. §102(a)

Claim 13 stands rejected under 35 U.S.C. §102(a) as
being clearly anticipated by Kimura (Japanese Patent No.
JP410004042A - Application No. JP08177051).

Reexamination and reconsideration of the Examiner's